Attorney's Docket No.: 10559-076002 / P7568C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Debra Bernstein et al. Art U Serial No.: Not yet assigned Exam

Art Unit : Unknown Examiner : Unknown

Filed: August 19, 2003

Title

: MICROENGINE FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 09/387,046, filed on August 31, 1999. The attached references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application:

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: Wyust 19, 2003

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Information Disc! sure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Debra Bernstein et al.		
		Filing Date August 19, 2003	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL	EP 0 745 933	12-4-96	EPO				
	AM	59111533	27-06-84	Patent Abstracts of Japan				
	AN							
	AO							
	AP							

	Other Documents (include Author, Title, Date, and Place of Publication)					
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	AQ	"Multithreaded Processor Architectures", G.T. Byrd et al., IEEE Spectrum, IEEE Inc., New York, US, Vol. 32, No. 8, 8-1-95, pps. 38-46.				
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	AT					

Examiner Signature	Date Considered				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with					
next communication to applicant.	,,				